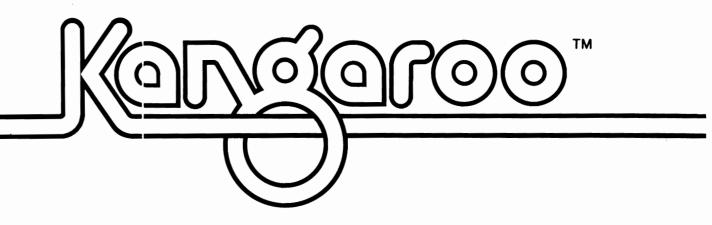
# Schematic Package Supplement to

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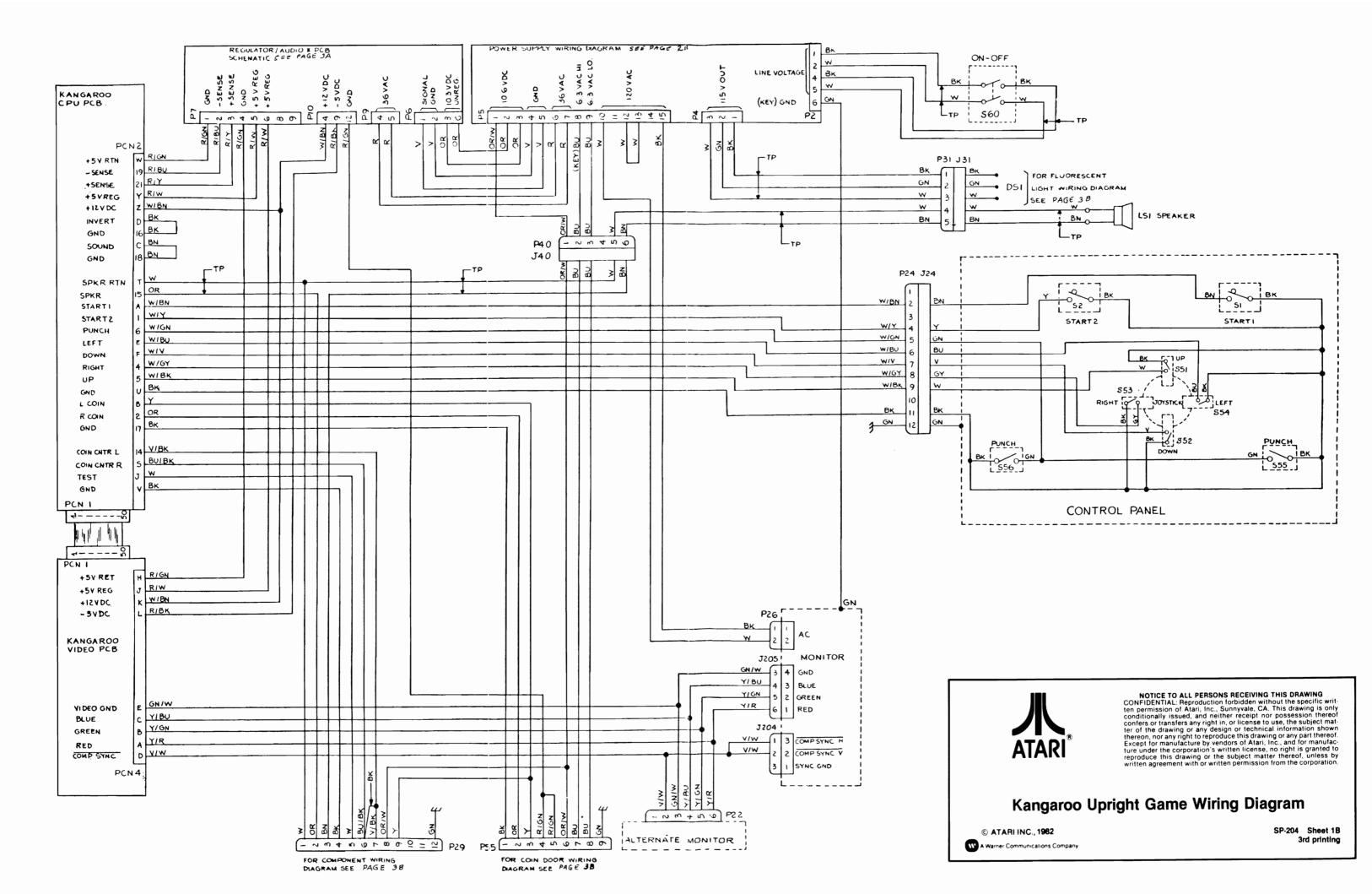
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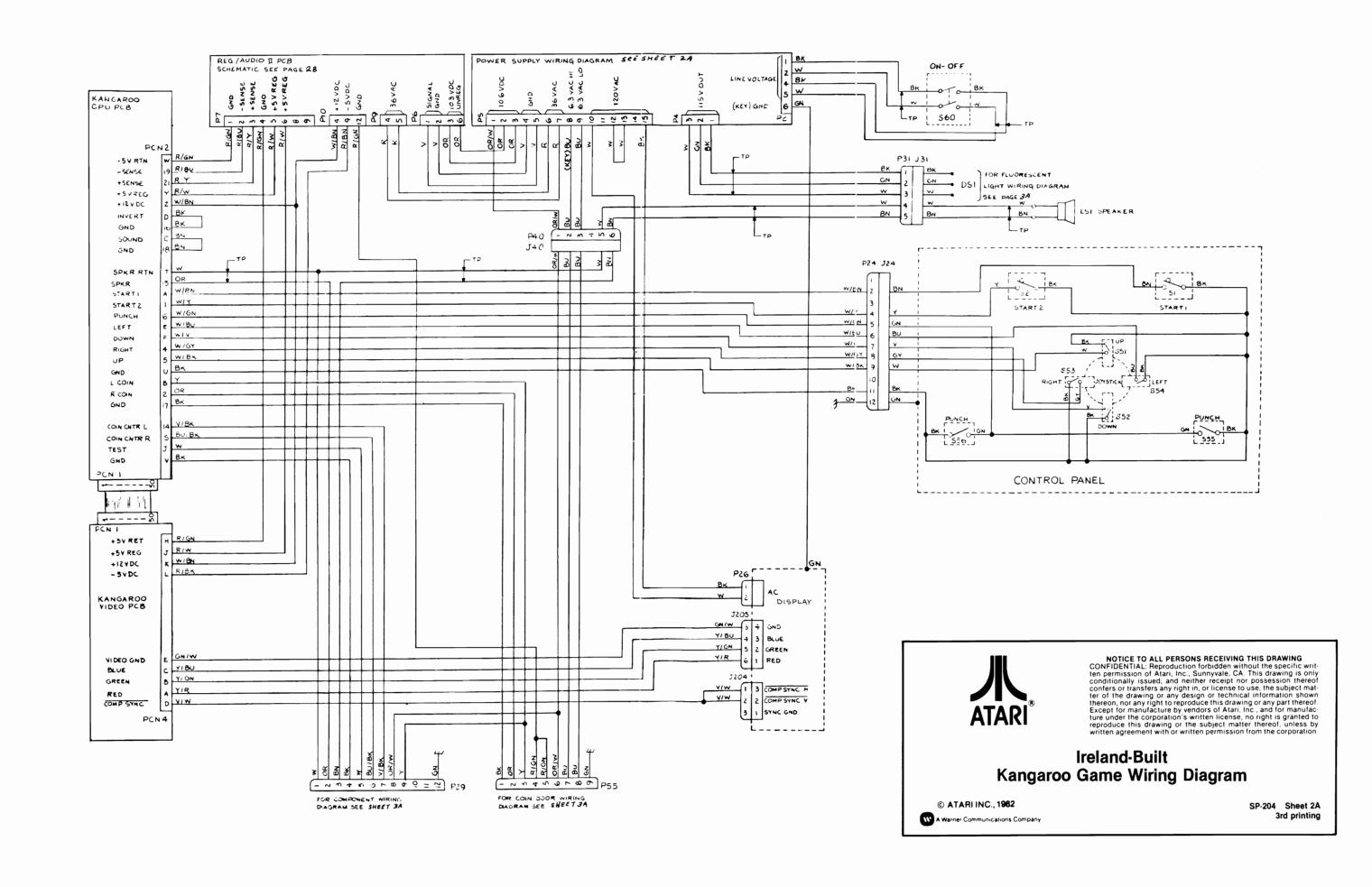
# Operation, Maintenance and Service Manual

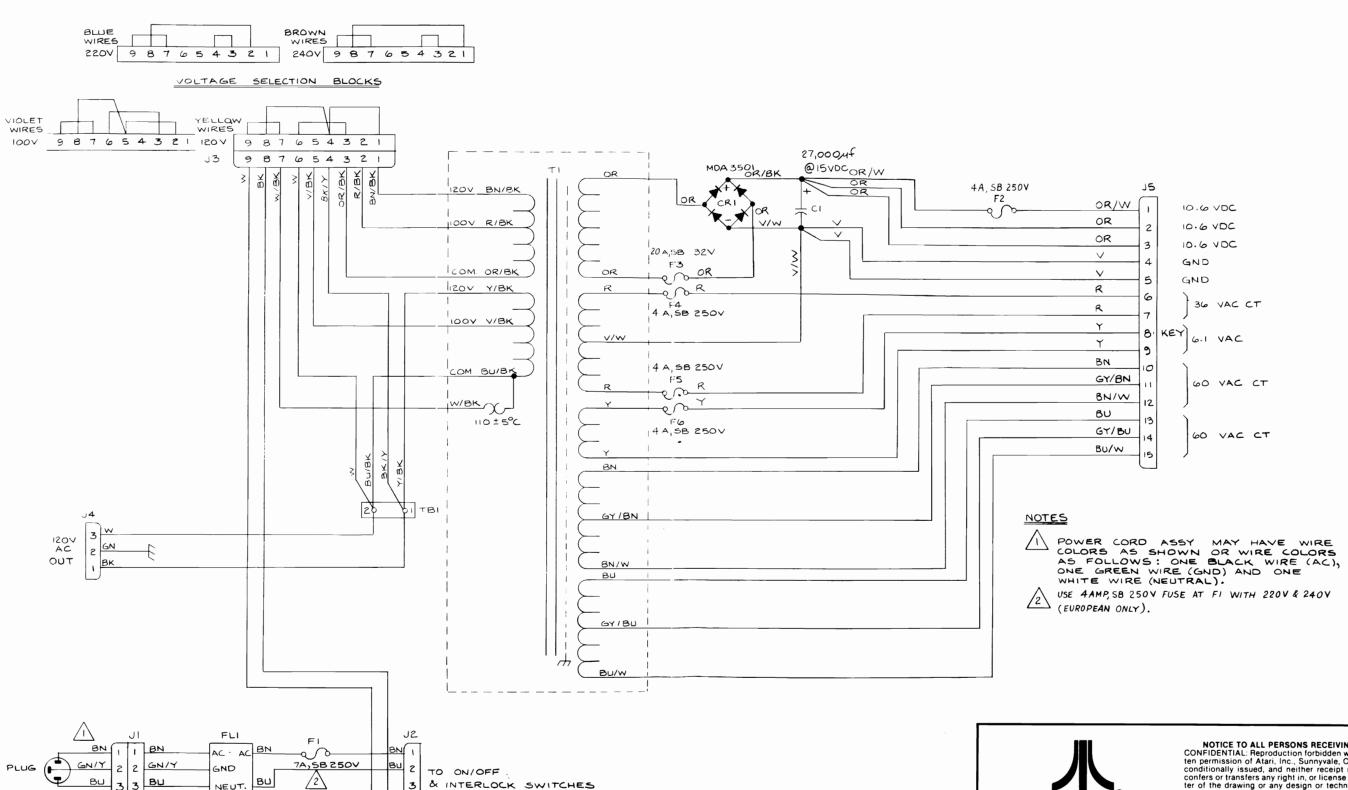
#### NOTE

This staple temporarily holds the schematic package together. Remove the staple before using the schematics.









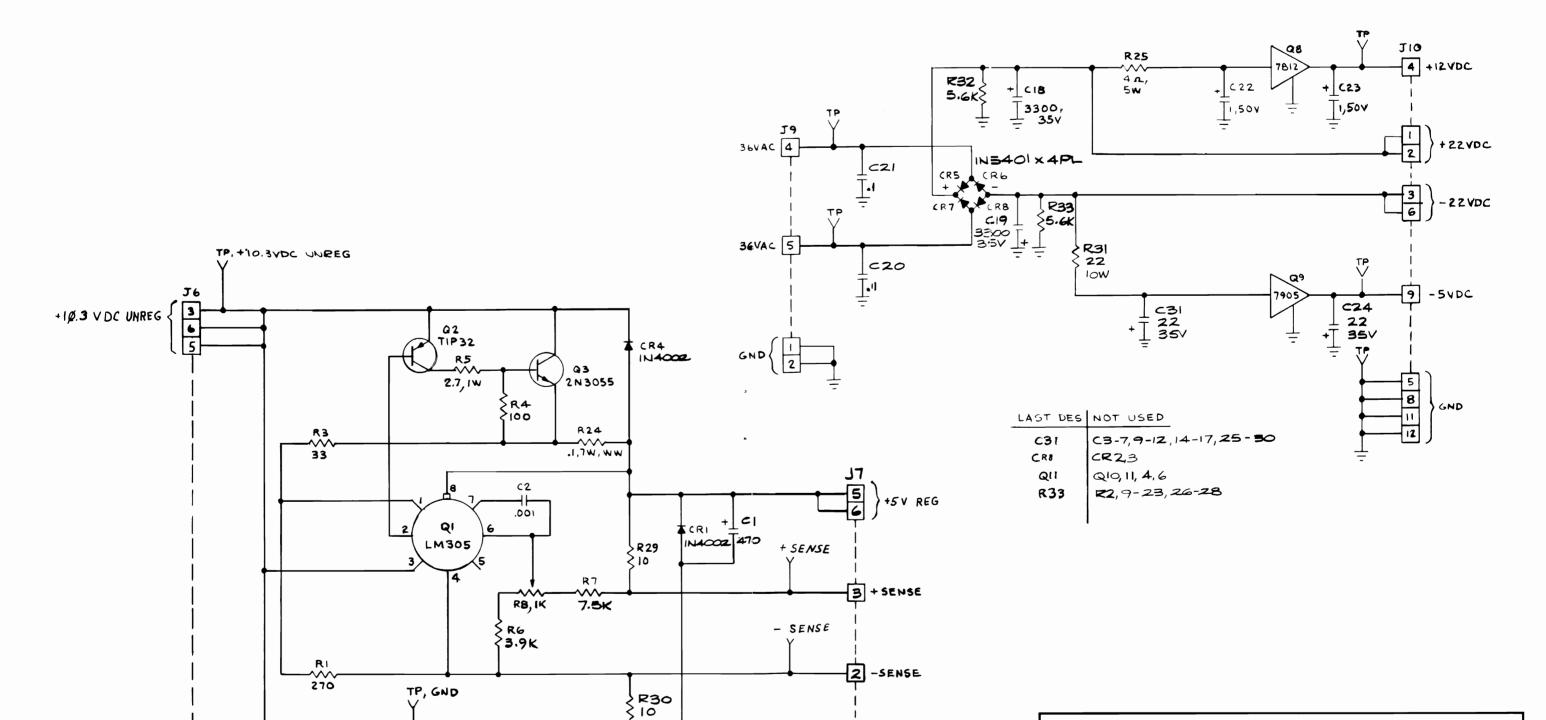


#### **Kangaroo Power Supply**

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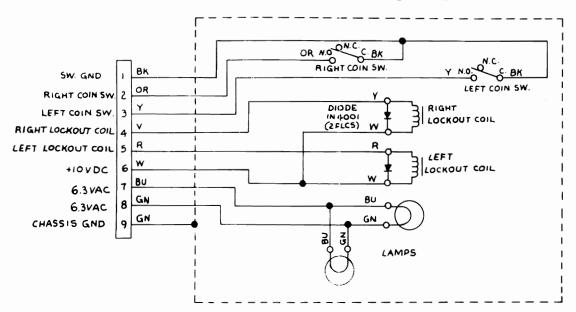
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#### Kangaroo Reg./Audio II PCB Schematic Diagram

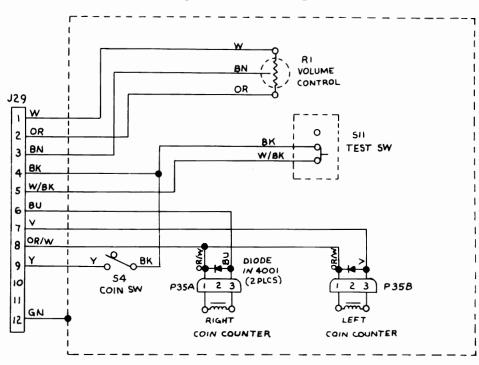
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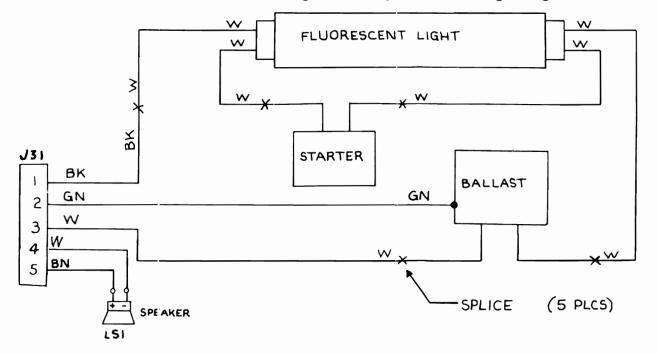
#### Coin Door Wiring Diagram



#### **Utility Panel Wiring Diagram**



#### Fluorescent Light and Speaker Wiring Diagram





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#### **Kangaroo Game Wiring Interfaces**

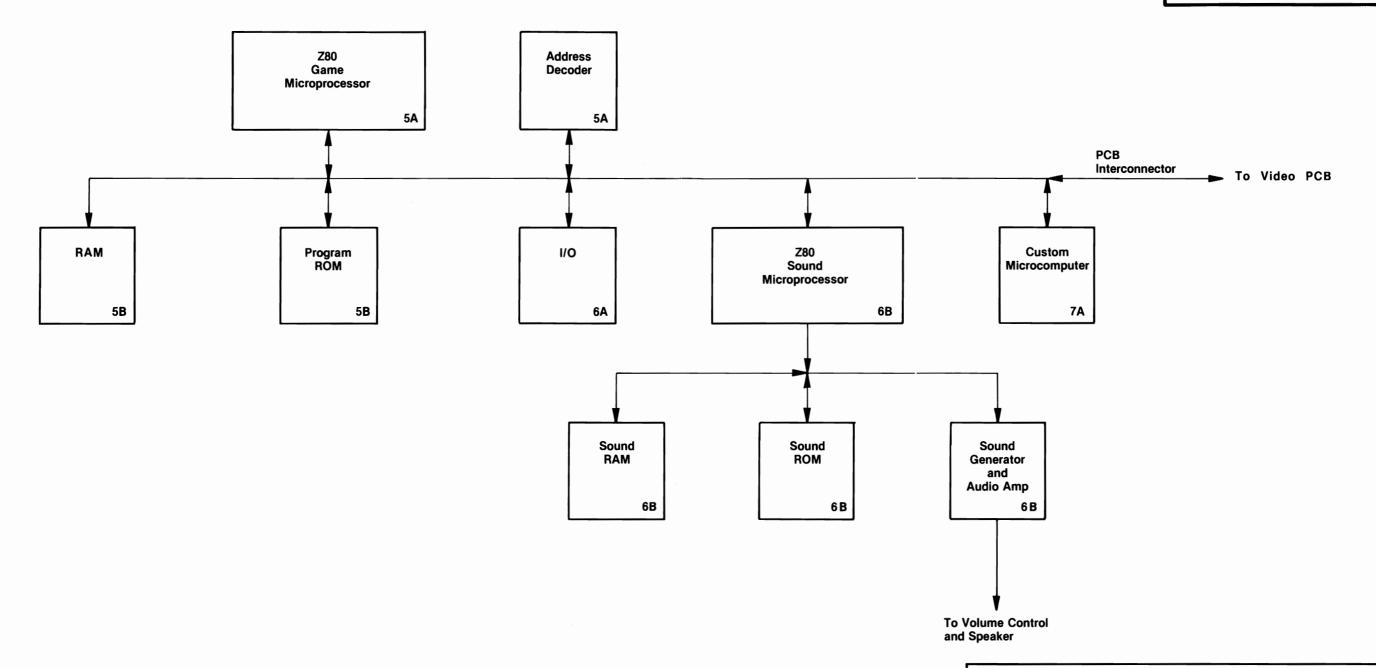
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#### NOTE -

The service switch, located on the CPU PCB, allows you to enter credits without tripping the coin counter.



**Block Diagram (CPU PCB)** 



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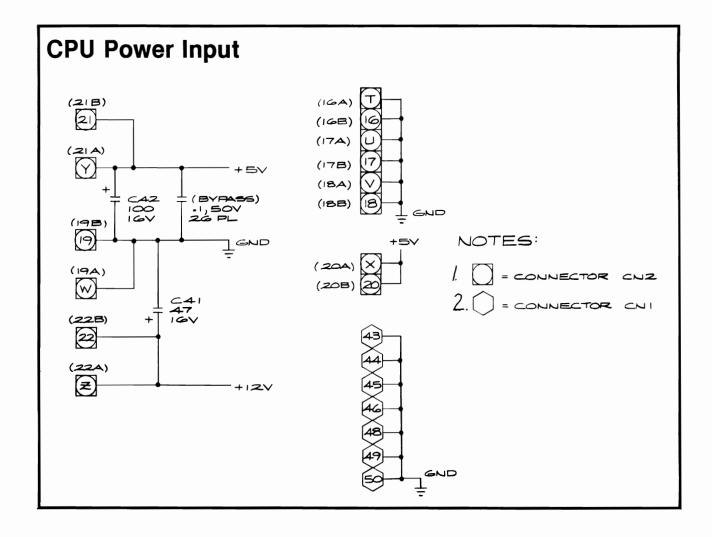
#### Kangaroo CPU PCB Schematic Diagram

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### **Memory Map**

								_					
HEXA- DECIMAL ADDRESS	R/W	D7	D6	D5		ATA D3	D2	D1	D0	FUNCTION			
	Game Microprocessor Memory Space (IC15)												
0000-5FFF	R	D	D	D	D	D	D	D	D	Z80 24K Program ROM			
E000-E3FF	R/W	D	D	D	D	D	D	D	D	1K Working RAM			
E400	R	D	D	D	D	D	D	D	D	Option Switch			
E800 E801 E802 E803 E804 E805 E806 E807 E808 E809 E80A	\$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$	0 0 0 0 0 0	D D D D D D	D D D D D D D D	D D D D D D D D	D D D D D D D D	D D D D D D D D	D D D D D D D D	00000000000	Low Byte Start Address of Data in Picture High Byte Start Address in Bit Map RAM (where High Byte picture is to be written) During DMA Low Byte Picture Size for DMA High Byte and DMA Start Vertical Start Address in Bit Map Horizontal Start Address in Bit Map Bank Select Latch A & B Bit Map Control Latch (A = playfield, B = motion) Color-Shading Latch			
EC00 EC00 EC00 EC00 EC00	W R R R R R	D	D	D	D	D D	D D	D D	D D	Sound DATA to Sound Microprocessor Utility Coin Switch 1 Player Start 2 Player Start Left Coin Input Right Coin Input			
ED00 ED00 ED00 ED00 ED00 ED00 ED00	W W R R R R R				D	D	D	D D	D D	Coin Counter 1 Coin Counter 2 (European games) Player 1 Right Player 1 Left Player 1 Up Player 1 Down Player 1 Punch			
EE00 EE00 EE00 EE00 EE00	RRRR				D	D	D	D	D	Player 2 Right Player 2 Left Player 2 Up Player 2 Down Player 2 Punch			
EFXX EFXX	W R					D D	D D	D D	ם ם	Output to Custom Microcomputer Input from Custom Microcomputer			
			S	oun	d M	icrop	oroc	ess	or M	lemory Space (IC34)			
0000-0FFF 4000-43FF	R R/W	D D	D D	D D	D D	D D	D D	D D	D D	4K Program ROM 1K Working RAM			
6000	R	D	D	D	D	D	D	D	D	Read DATA from Game Microprocessor			
7000 8000	W R	D D	D D	D D	D D	D D	D D	D D	D D	Write to Sound Chip (GI-AY-3-8910) Read from Sound Chip			



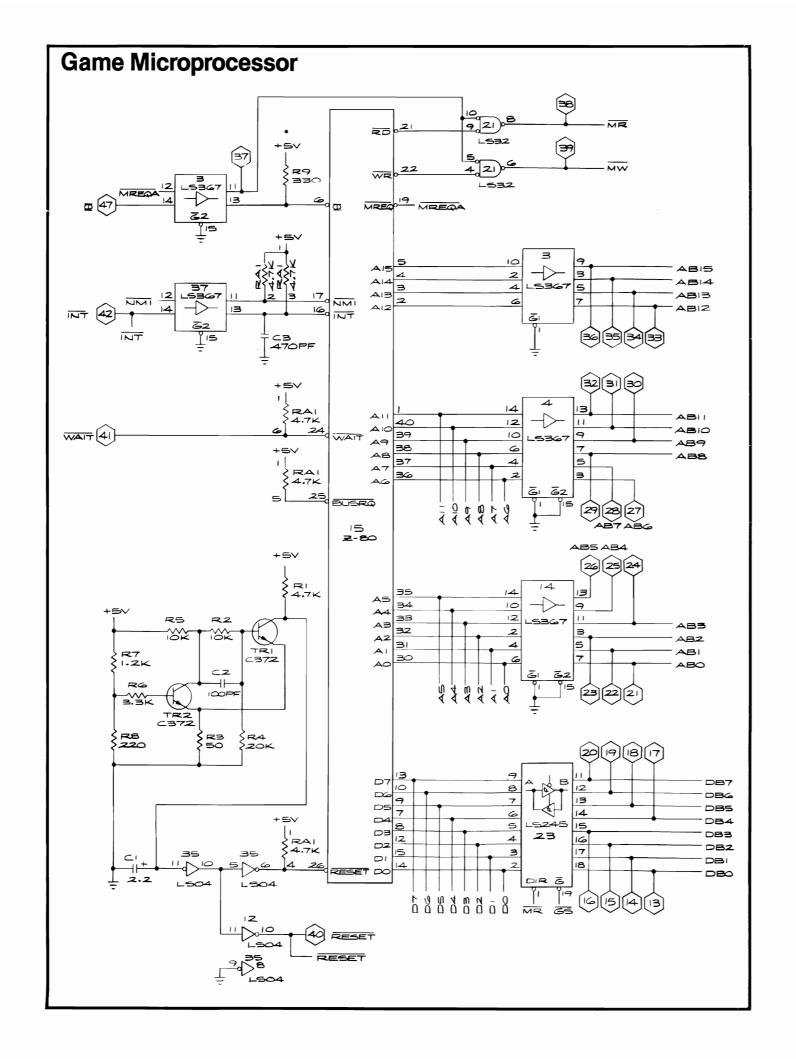


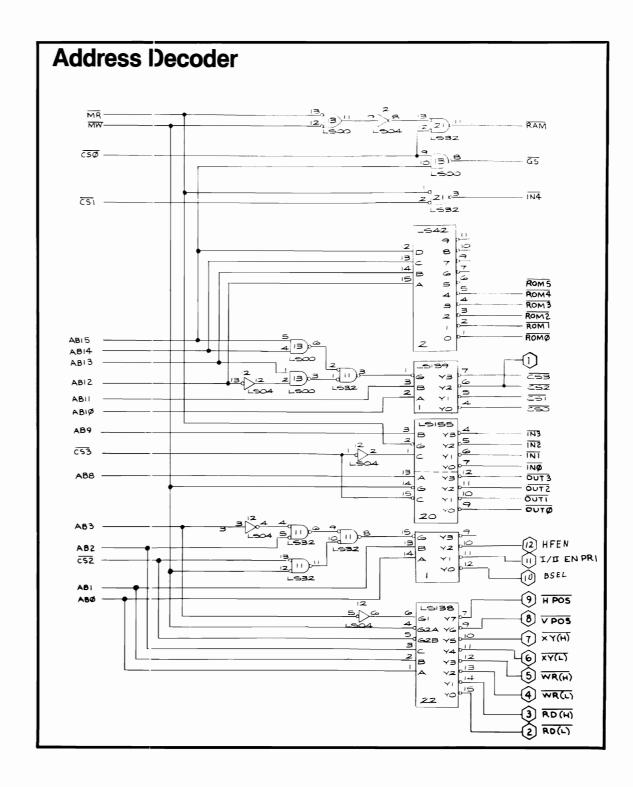
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#### Kangaroo CPU PCB Schematic Diagram



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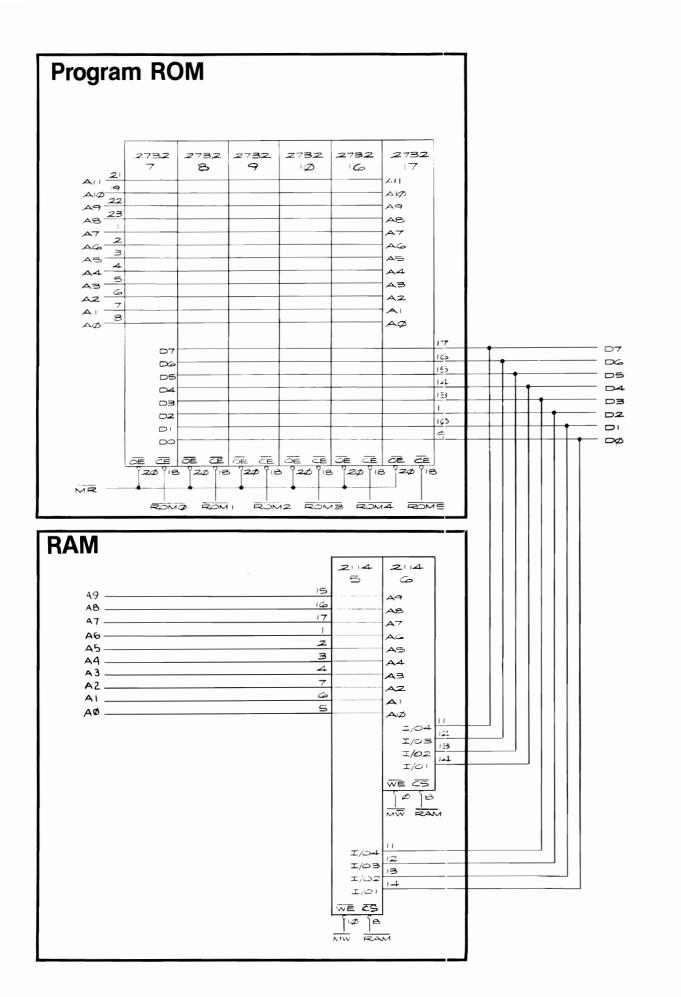


#### Kangaroo CPU PCB Schematic Diagram

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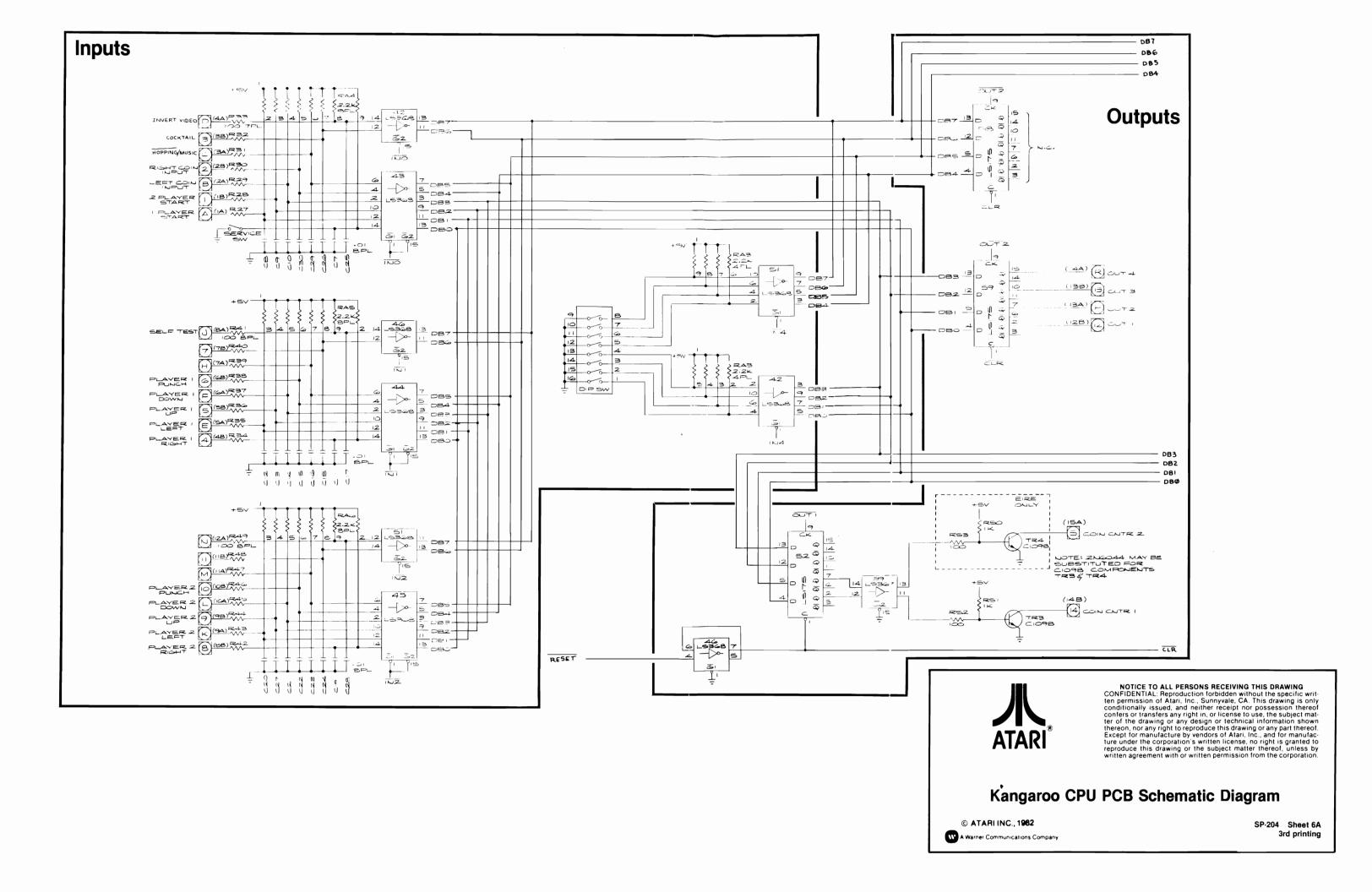


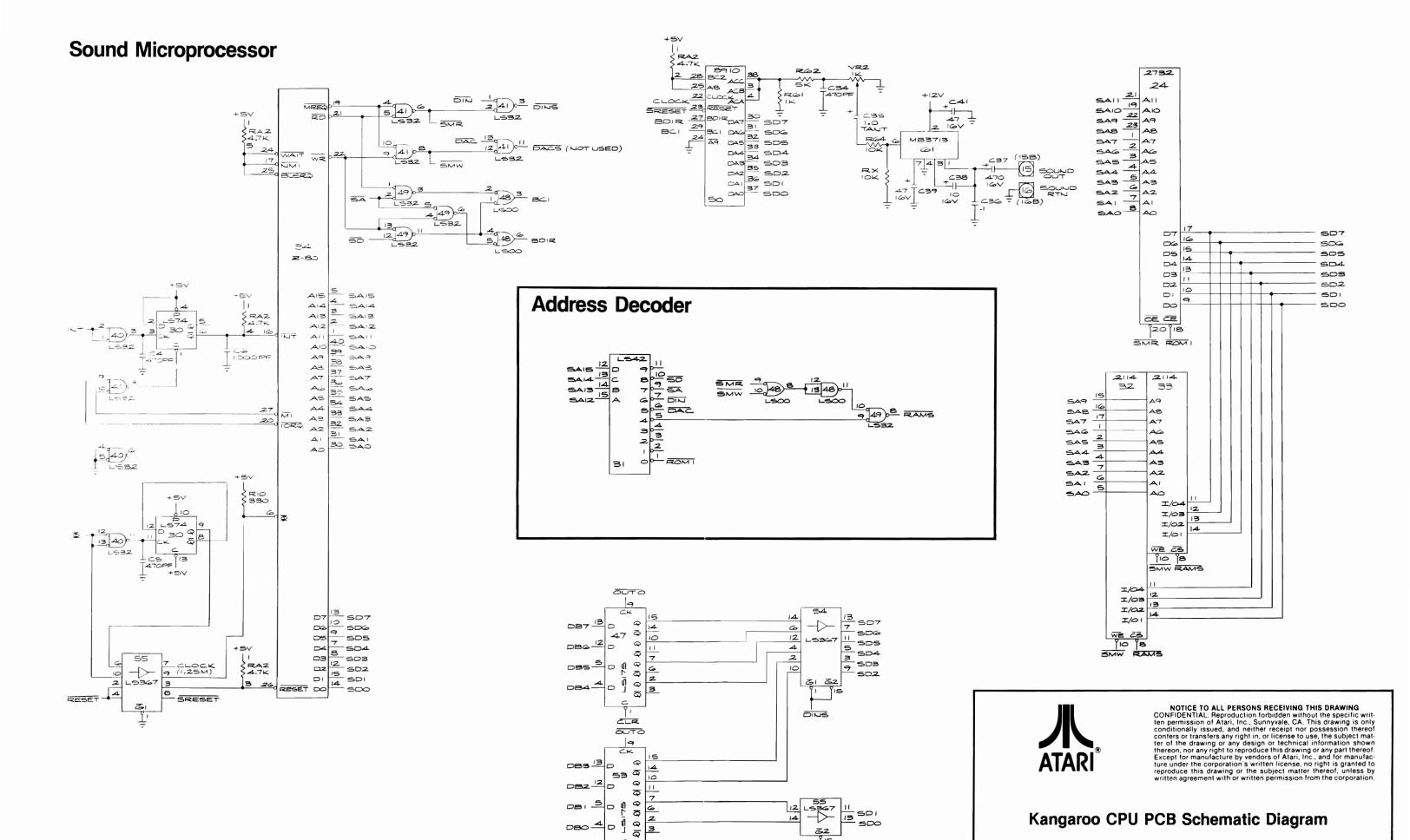
#### Kangaroo CPU PCB Schematic Diagram

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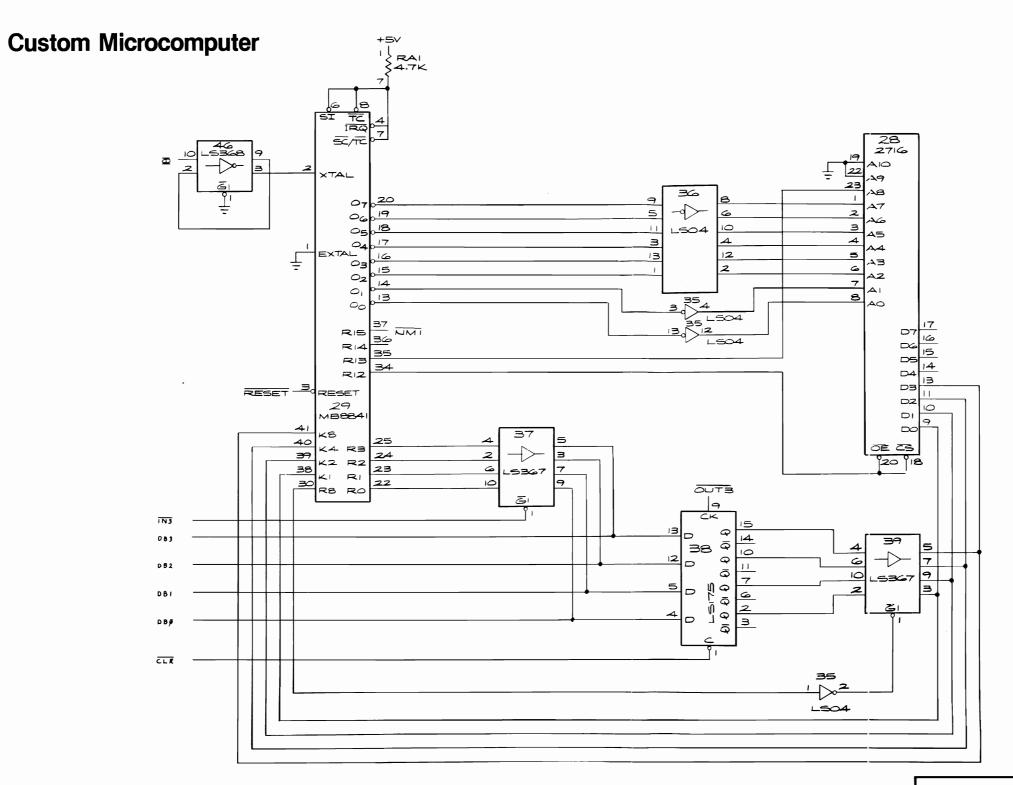
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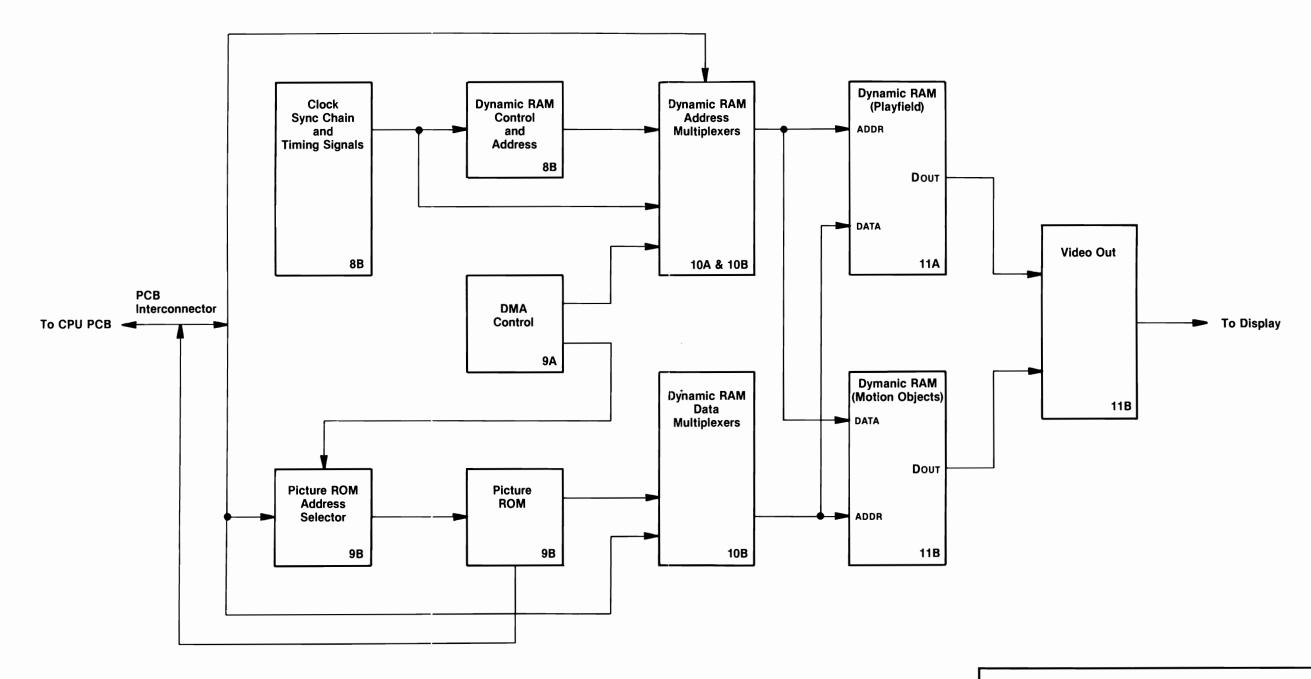


### Kangaroo CPU PCB Schematic Diagram

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### **Block Diagram (Video PCB)**





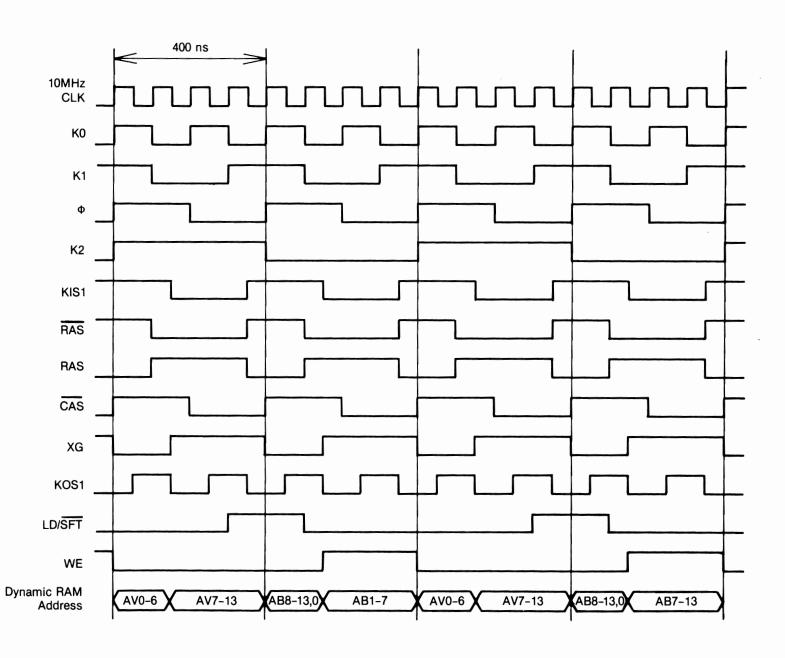
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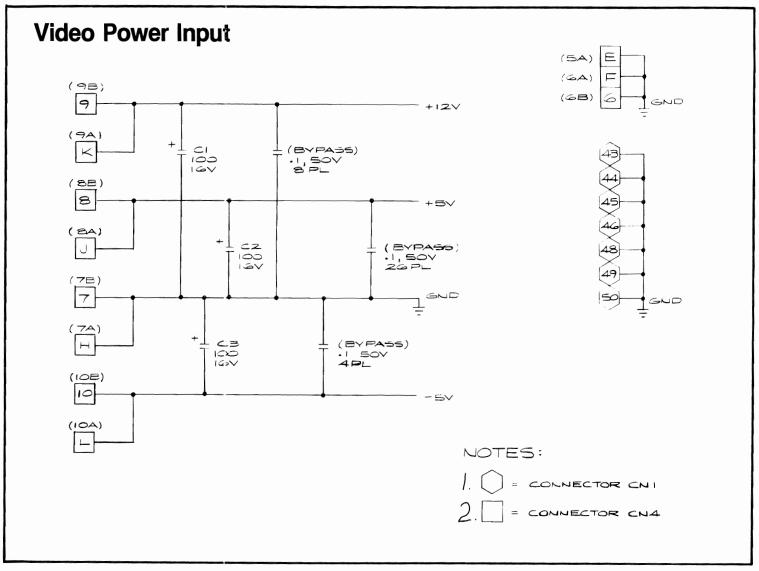
#### Kangaroo Video PCB Schematic Diagram

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## **Dynamic RAM Timing Diagram (Video PCB)**







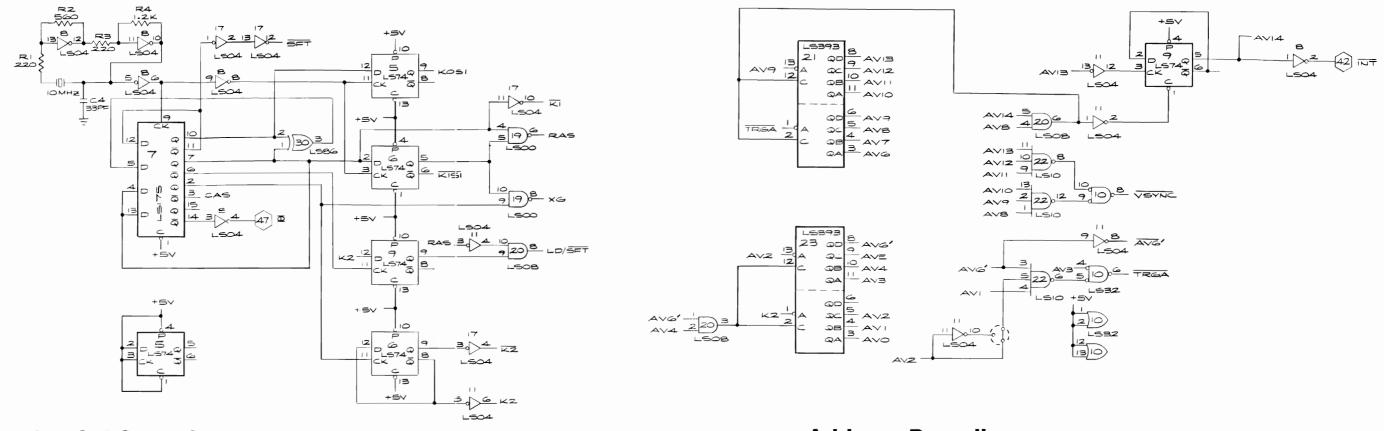
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### Kangaroo Video PCB Schematic Diagram

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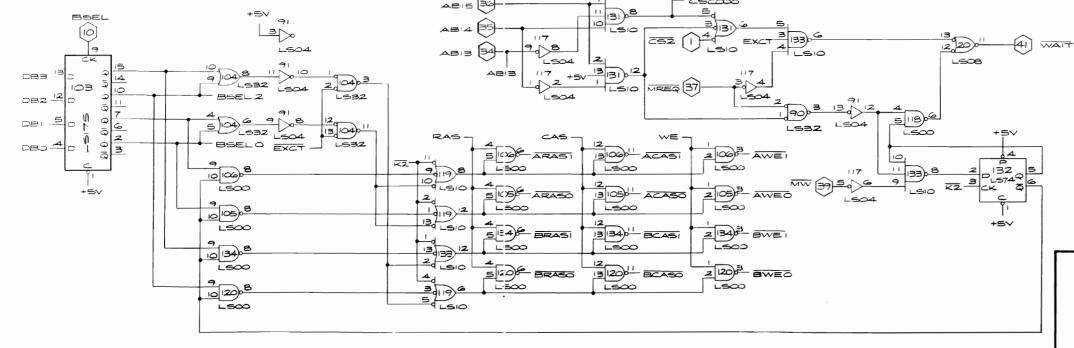
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### Clock, Sync Chain, and Timing Signals



### **Dynamic RAM Control**

### **Address Decoding**





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#### Kangaroo Video PCB Schematic Diagram

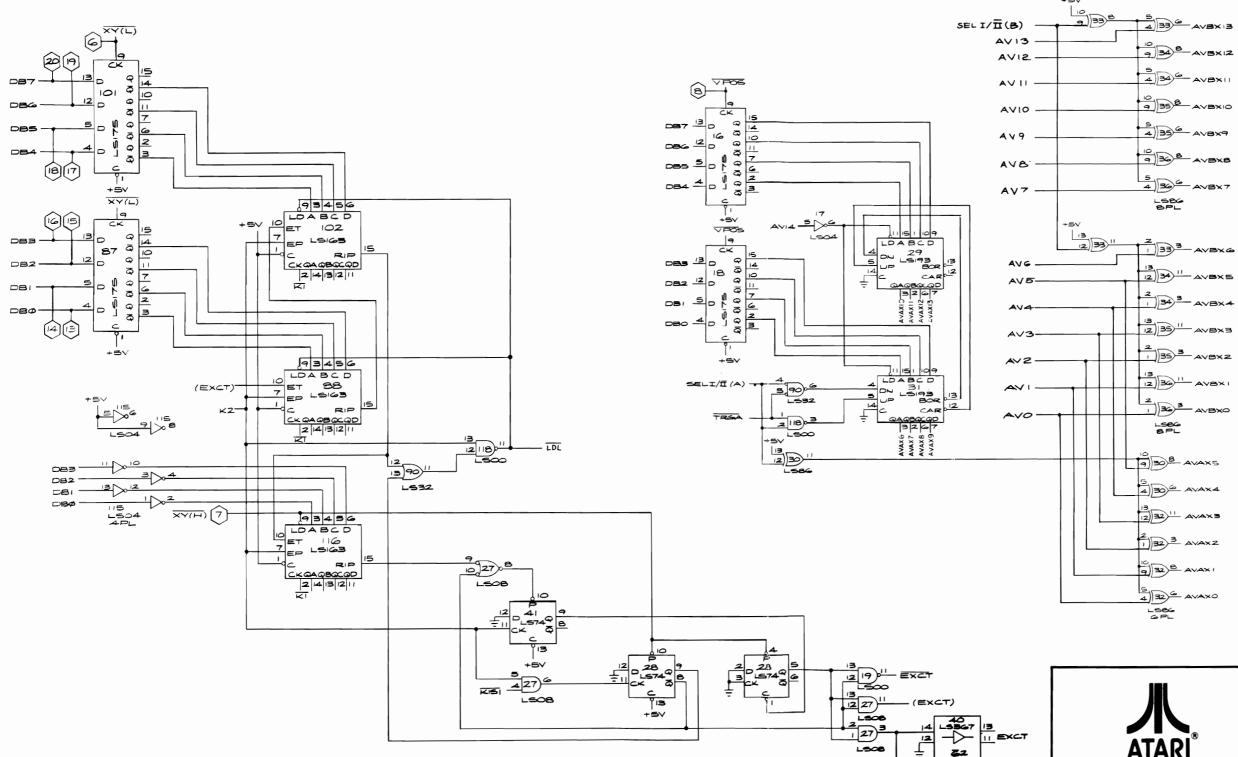
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#### **DMA Control**

### **Dynamic RAM Video Address and Flip**

[EXCT]





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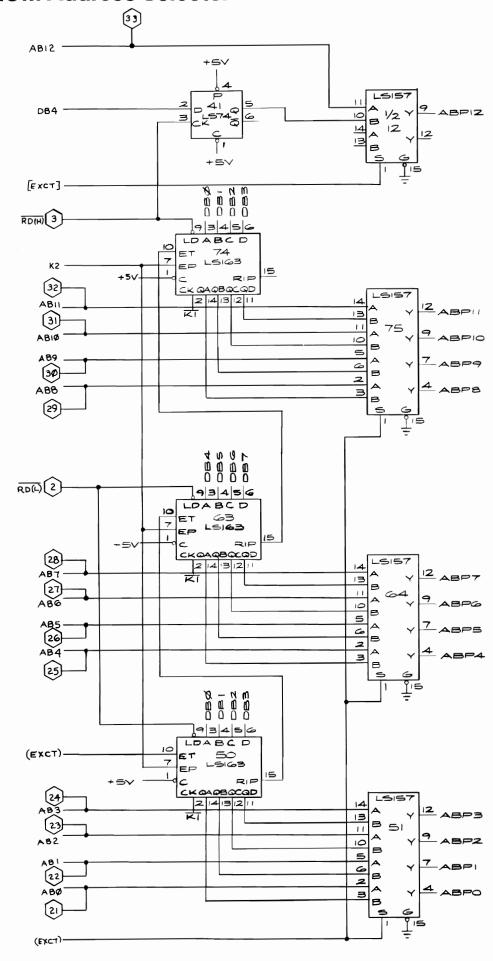
#### Kangaroo Video PCB Schematic Diagram

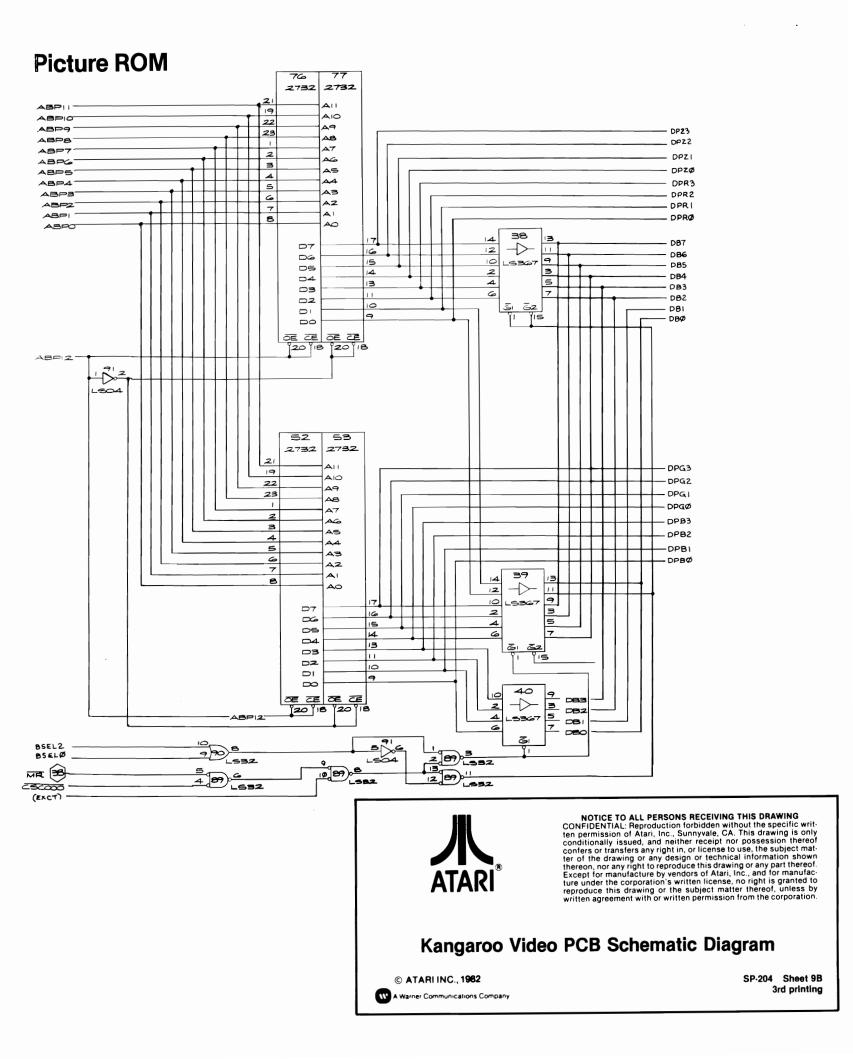
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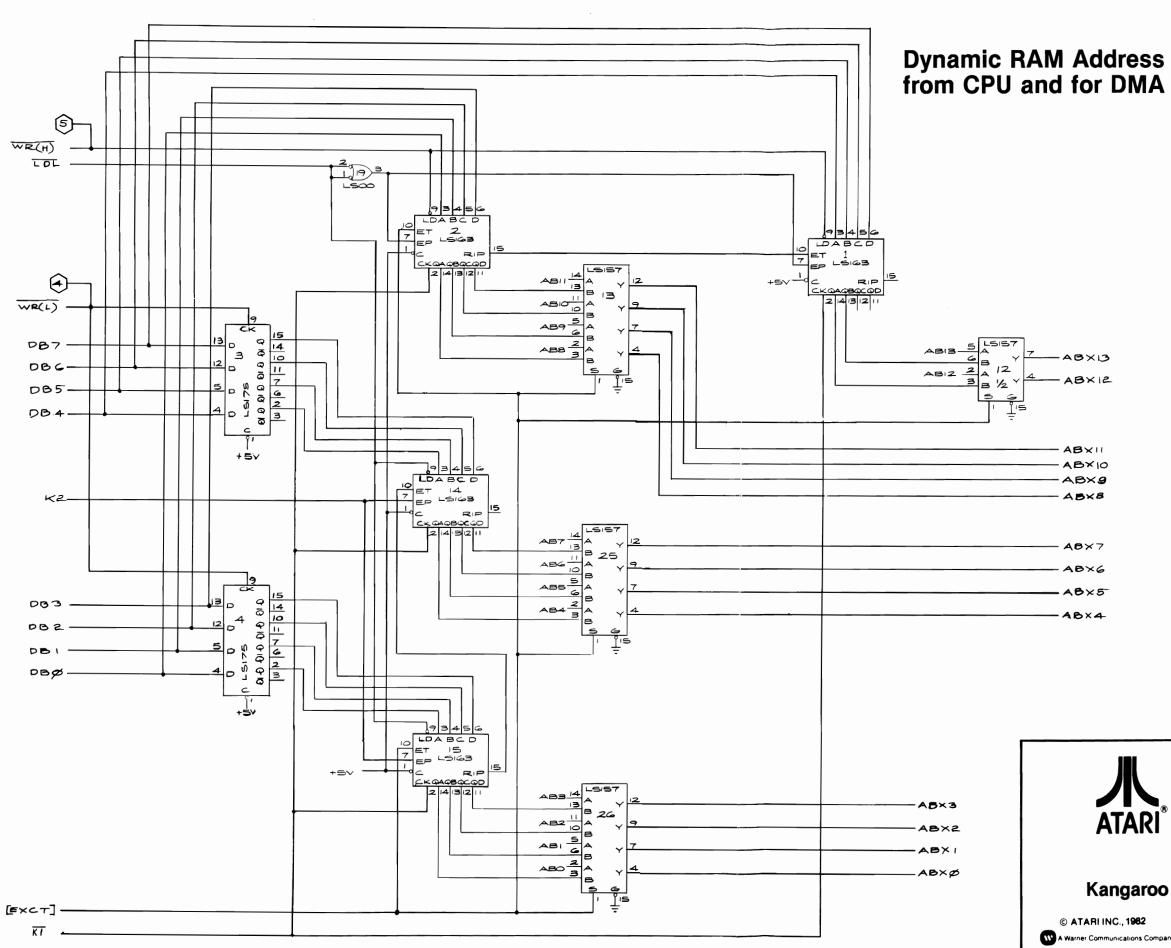
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#### **Picture ROM Address Selector**









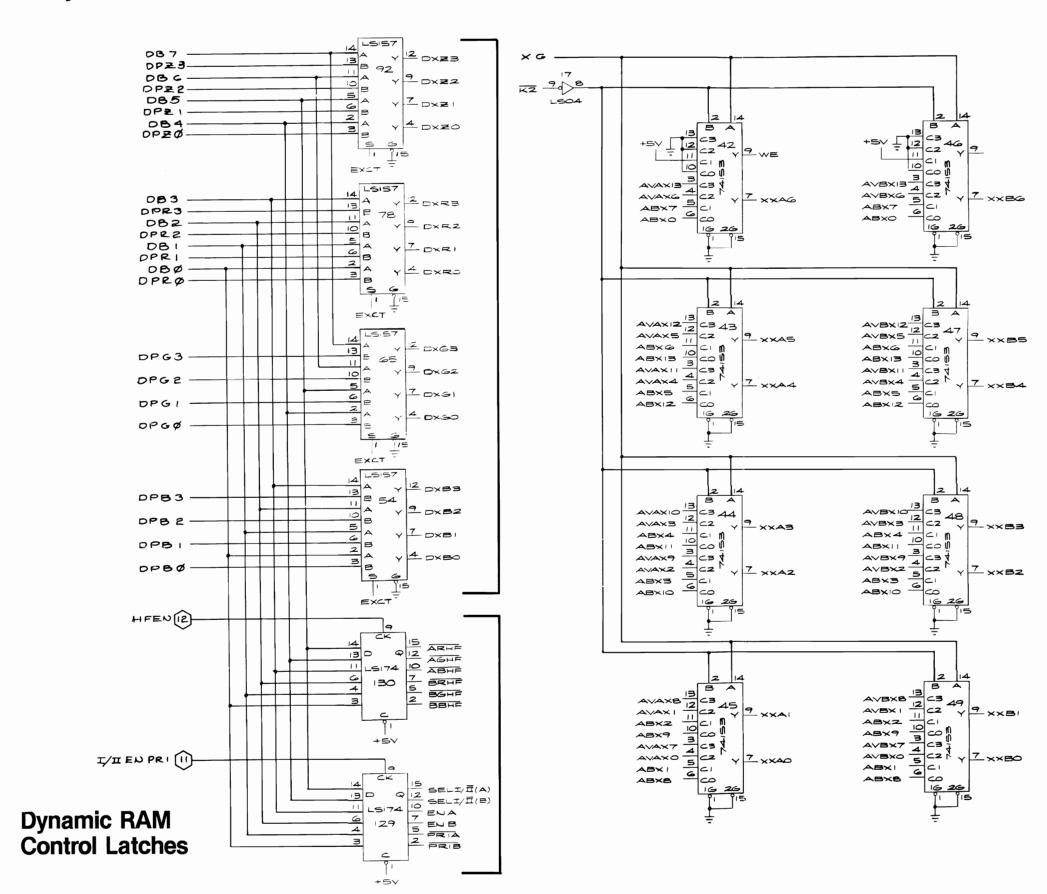
#### Kangaroo Video PCB Schematic Diagram

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### **Dynamic RAM Data Selector**

### **Dynamic RAM Address Selector**





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#### Kangaroo Video PCB Schematic Diagram

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